

Figure 1 - Z2002A Attenuator Test System Plan

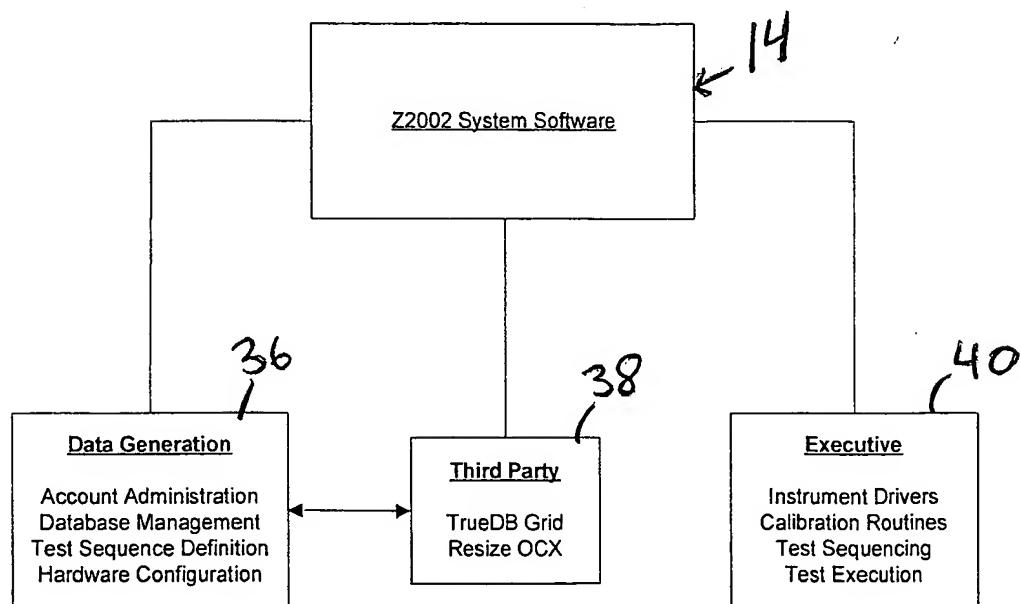


Figure 3.1 - Z2002A Software Block Diagram

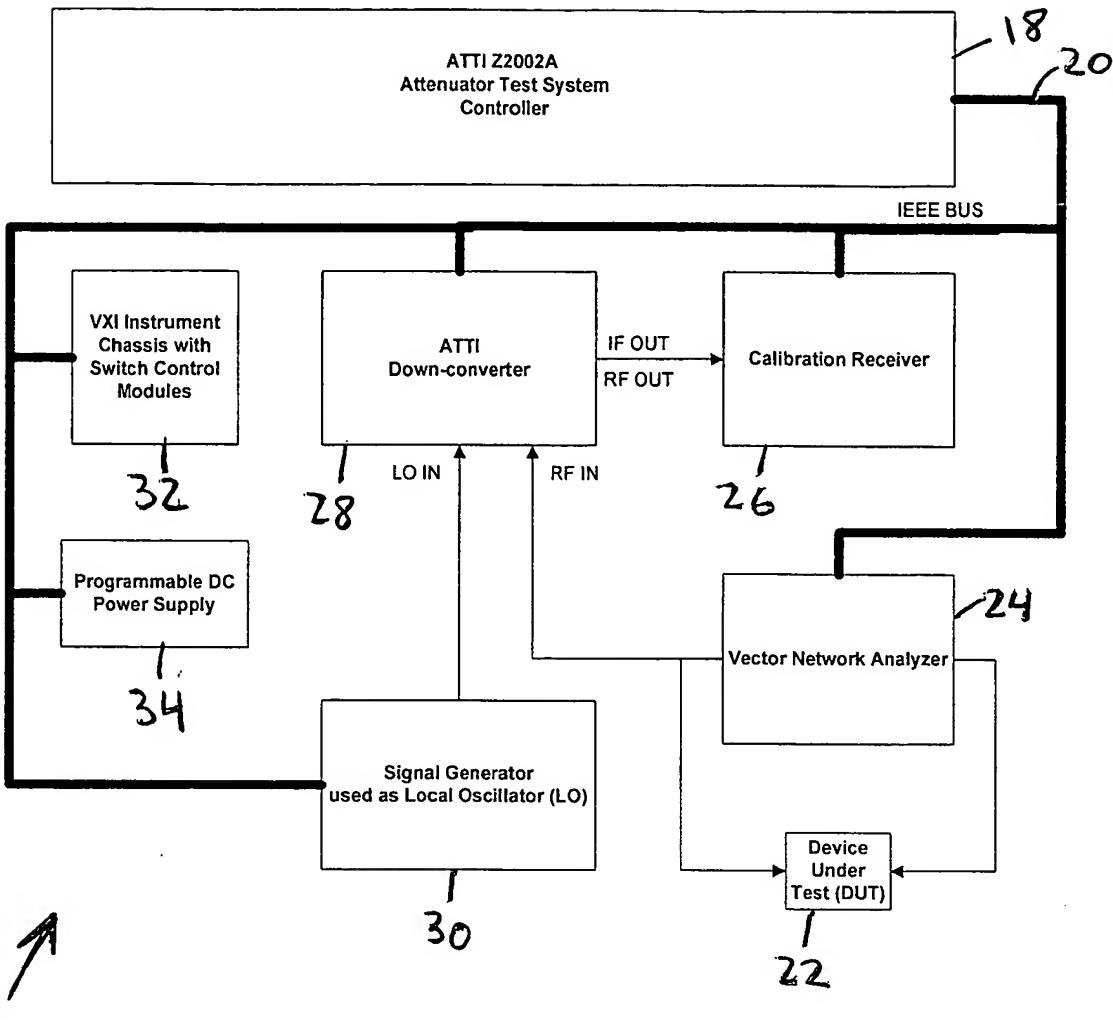


Figure 2 - Z2002A Hardware Block Diagram

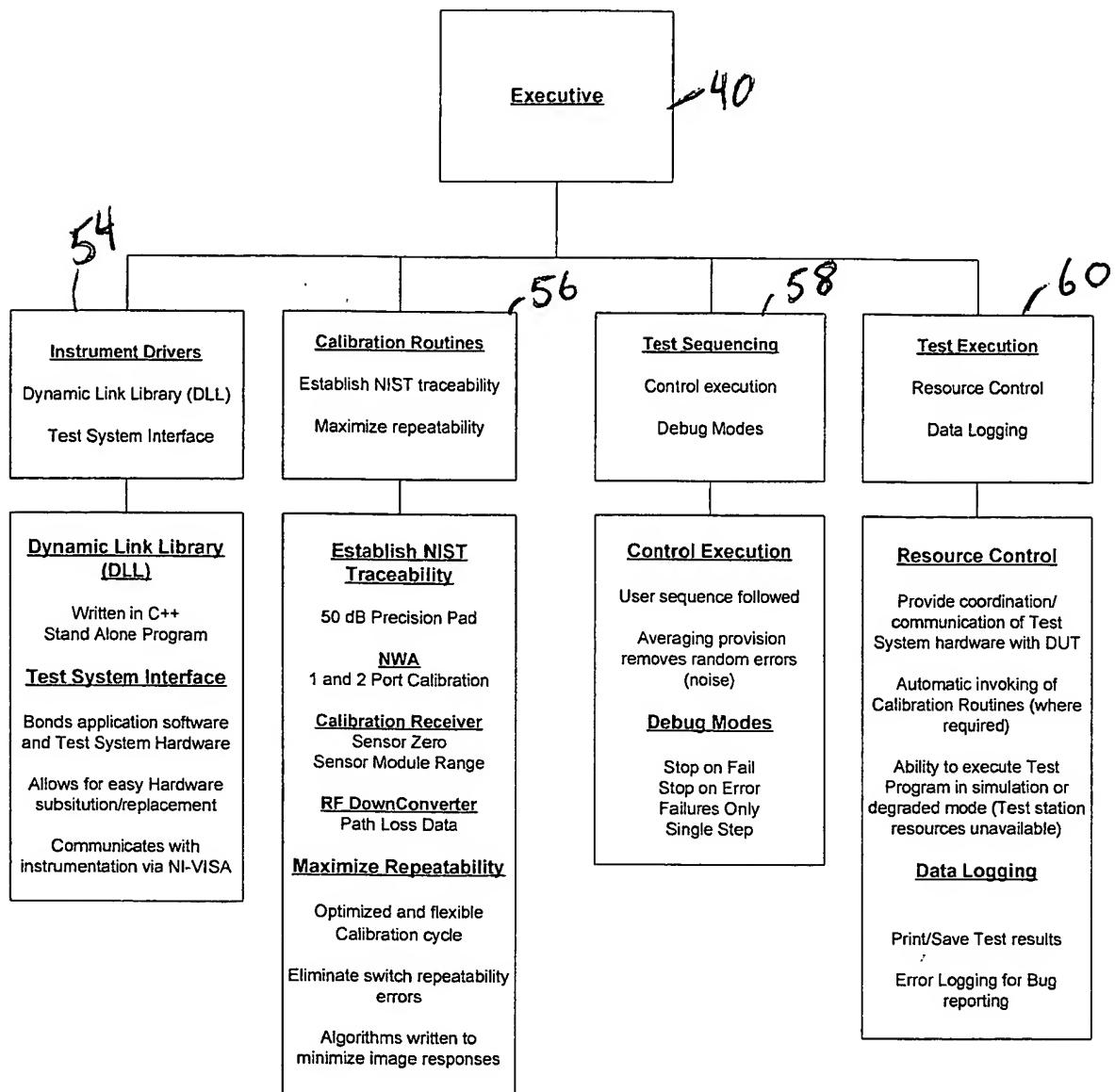


Figure 3.2 - Z2002A Executive Block Diagram

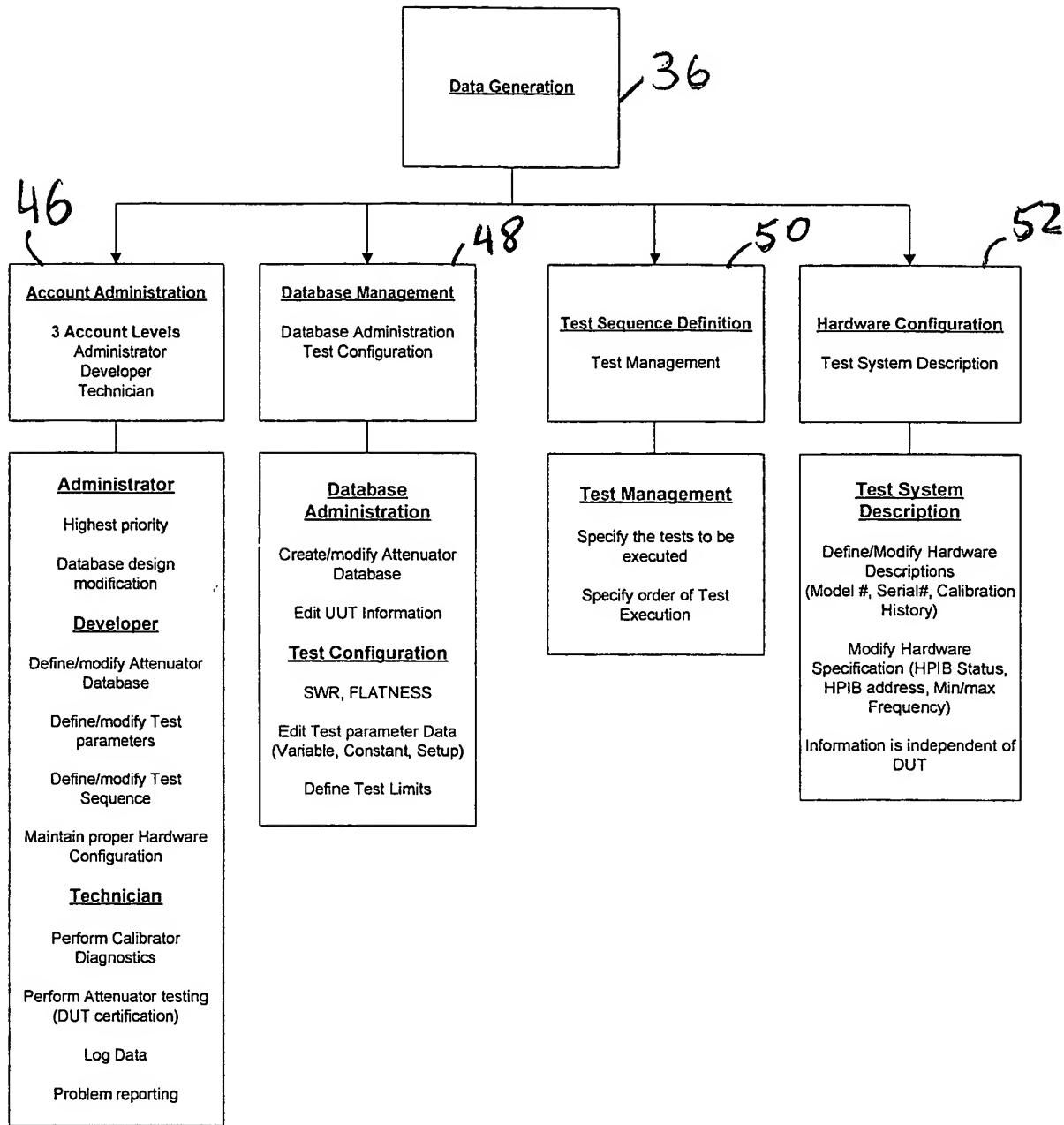


Figure 3.3 - Z2002A Data Generation Block Diagram

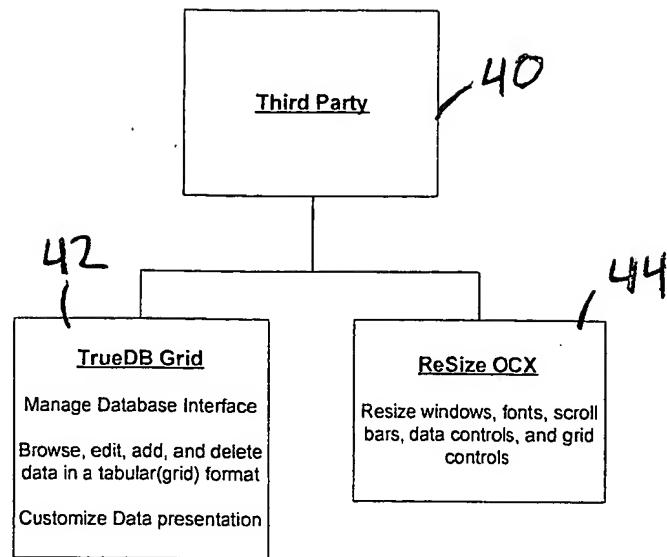


Figure 3.4 - Z2002A Third Party Block Diagram

ATTENUATOR FLATNESS TEST

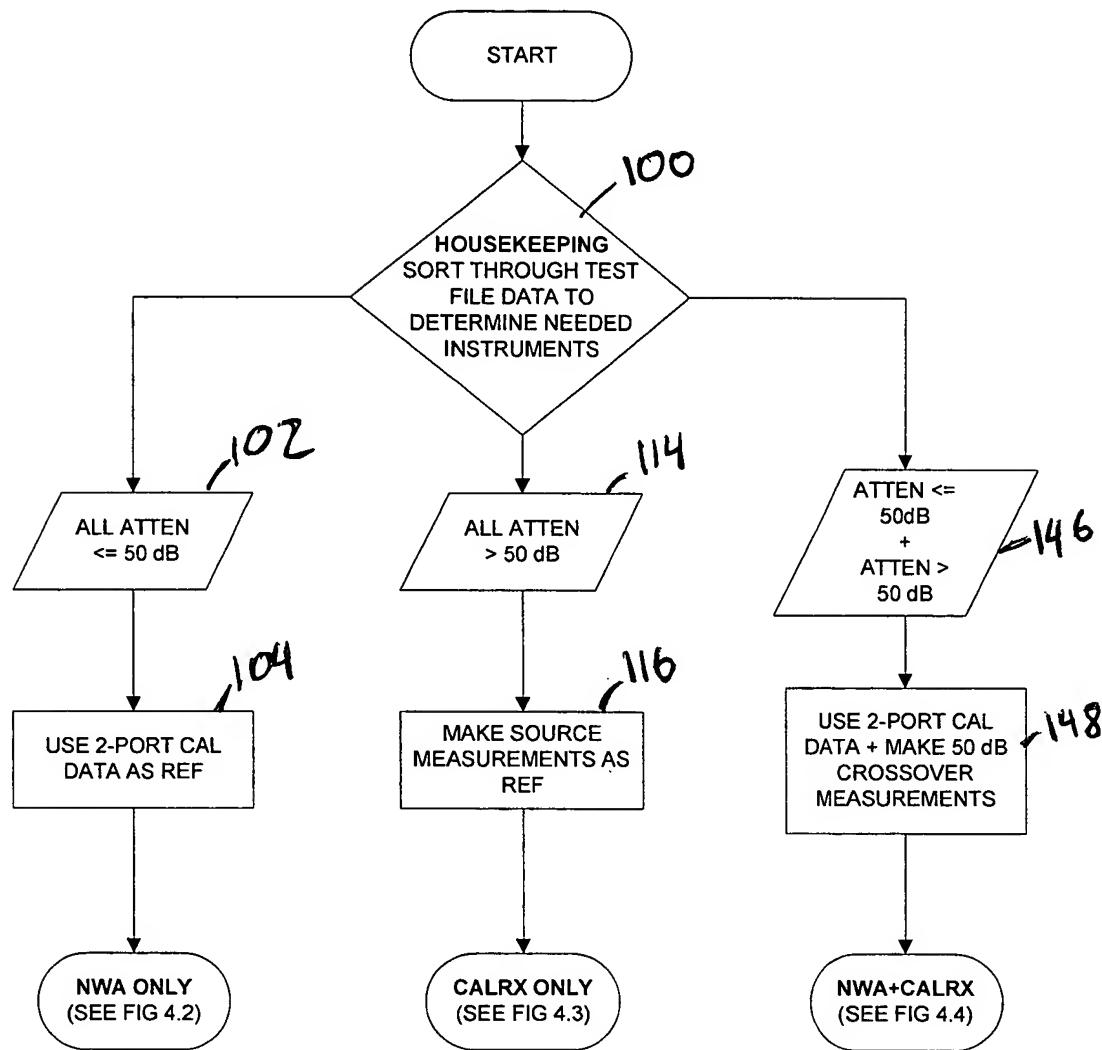


Figure 4.1 - Z2002A Flatness Test Flowchart

ATTENUATOR FLATNESS TEST

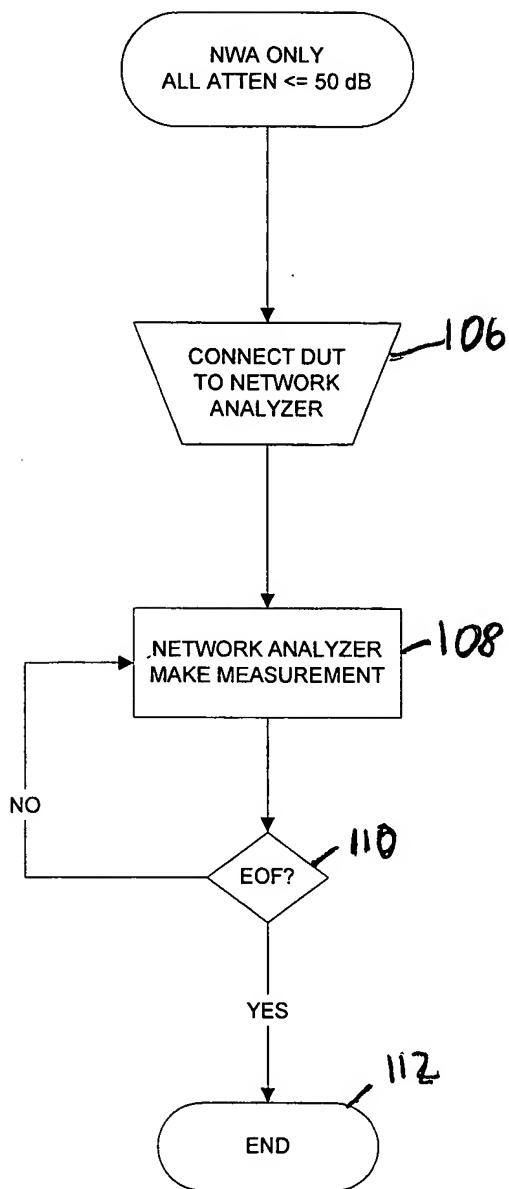


Figure 4.2 - Z2002A Flatness Test Flowchart

ATTENUATOR FLATNESS TEST

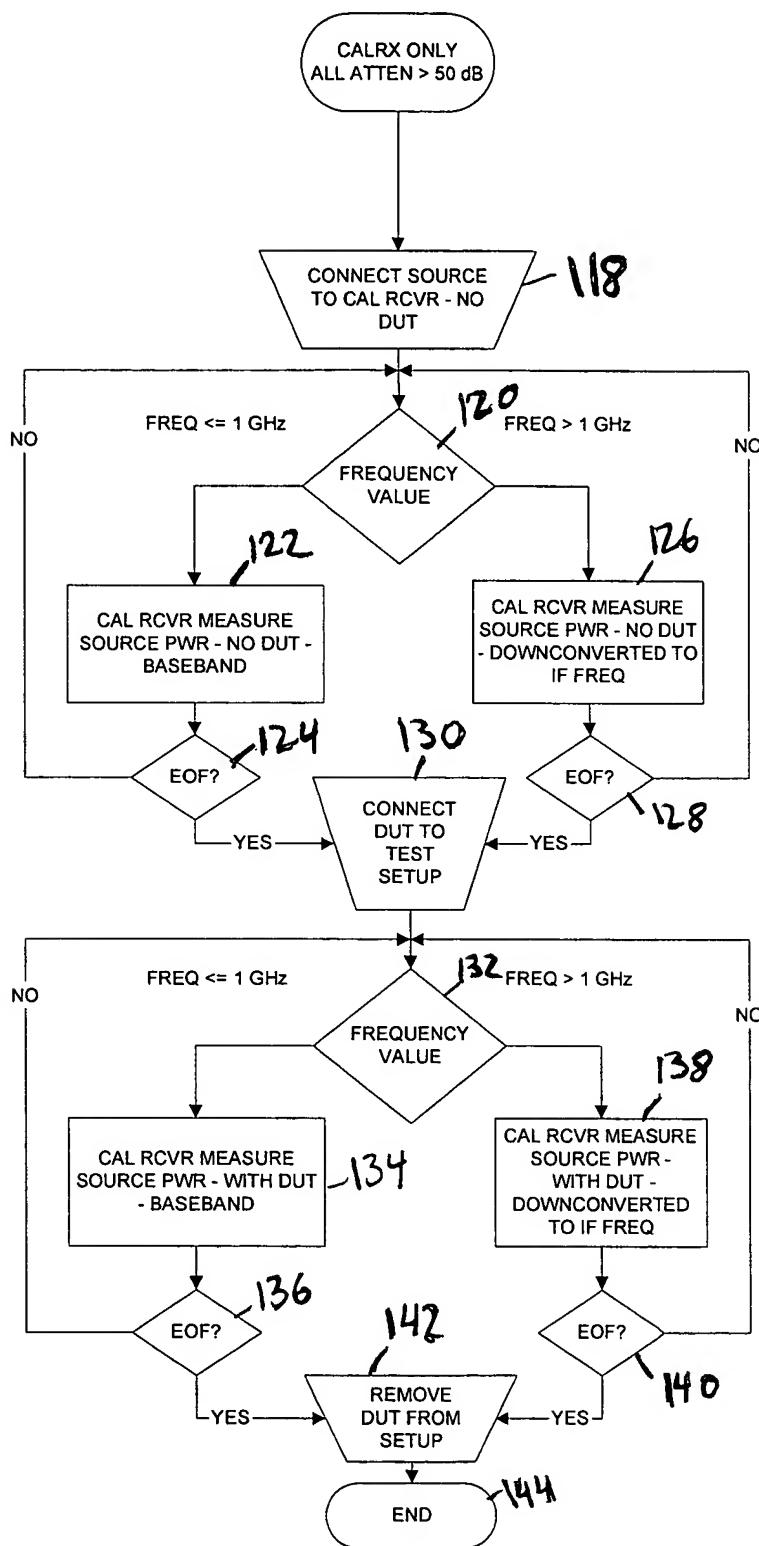


Figure 4.3 - Z2002A Flatness Test Flowchart

ATTENUATOR FLATNESS TEST

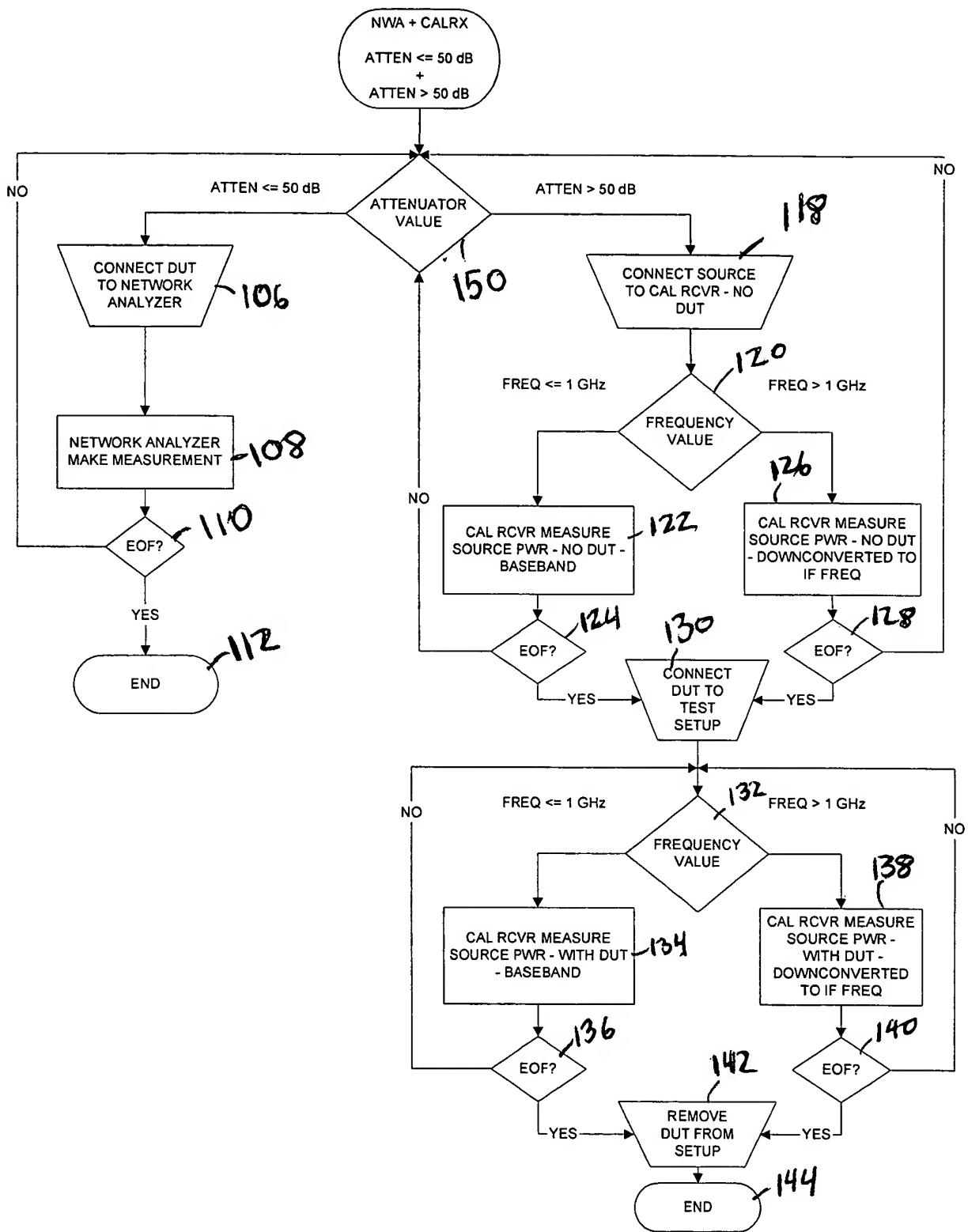


Figure 4.4 - Z2002A Flatness Test Flowchart

ATTENUATOR SWR TEST

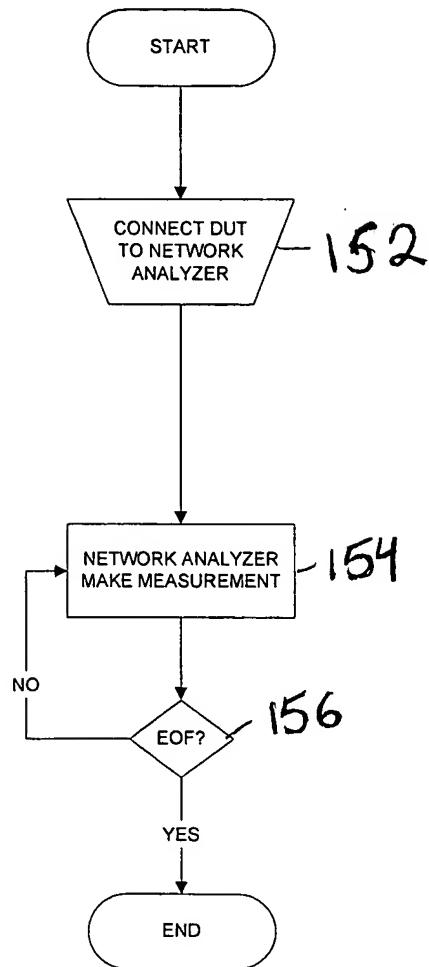


Figure 5 - Z2002A SWR Test Flowchart